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Docket No. BU998050DIV2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Is the patent application of
William P. Moore, et al.

Serial No.: 09/805,137

Group Art Unit: 2825

Filing Date: March 14, 2001

Examiner: Annette M. Thompson

For: MICROPROCESSOR INCLUDING CONTROLLER FOR REDUCED POWER
CONSUMPTION AND METHOD THEREFOR

Honorable Commissioner of Patents
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Under the provisions of 37 CFR §1.97 through §1.99 and pursuant to applicant's duty of disclosure under 37 CFR §1.56, applicant respectfully brings the following documents listed on the attached from PTO-1449, to the attention of the Examiner in charge of the above-identified application. All of these references were cited by the Examiner in co-pending Application No. 09/805,138, in an Office Action dated November 4, 2003.

This citation does not constitute an admission that the references are relevant or material to the claim. They are only cited as constituting related art of which the applicant is aware.

I hereby certify that no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in §1.56(c) more than three months prior to the filing of the information disclosure statement.

It is respectfully requested that the listed references be considered by the Examiner and formally made of record in this application.

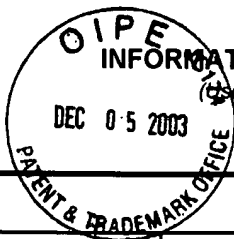
Please charge any deficiencies in fees and credit any overpayment of fees to Assignee's Deposit Account No. 09-0456.

Respectfully submitted,

Peter A. Balnave
Registration No. 46,199

Date: 12/5/03
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INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

ATTY-DOCKET NO.

BU998050DIV2

SERIAL NO.

09/805,137

APPLICANT(S)

William P. Moore, et al.

FILING DATE

March 14, 2001

GROUP

2825

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	5,867,644	02/02/99	Ranson, et al.			
	6,237,101 B1	05/22/01	Moore, et al.			
	5,862,148	01/19/99	Typaldos, et al.			
	2002/0013912 A1	01/31/02	Lin			
	5,732,255	03/24/98	Verbauwhede			
	5,276,824	01/04/94	Skruhak, et al.			
	5,790,874	08/04/98	Takano, et al.			

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

			J. Patrick Brennan, et al. "Low Power Methodology and Design Techniques for Processor Design", 1998, Pages 268-273.

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.